Quantum Computing @Microsoft is hiring!

Microsoft has been studying quantum computing for several years and has become the world’s center of expertise on topological quantum computing. The research effort includes a staff of theoretical physicists and experimental teams around the world. Our team is building a controls system for quantum computing that is based on a classical computer architecture. The overall system is being developed by a multidisciplinary team that includes software and hardware architects. The control system is a general-purpose computing platform that can perform complex, high performance computing functions.

We are looking for both specialists and well-rounded generalists. Technology areas where previous experience in shipping products or advanced stage incubation are valuable to our efforts include any of the following: Multicore CPU/GPU architectures, Graphics Architectures, Advanced System Architectures, Mobile Architectures and modern SOCs, Low power IP development.

Key responsibilities include:
- Contribute to the development of the pre/post silicon verification strategy, test design and test infrastructure for our products
- Work collaboratively with a team of diverse and talented peers in various disciplines including design, hardware, program management and software development for sustained long term success
- Participate actively in detailed design, code reviews, bug/issue triage with the feature teams and support well informed decisions towards business and engineering goals
- Partner with architects and other technical leaders to provide test influence on feature and product designs
- Maintain an unwavering focus on our customers and the value you can deliver to them
- Closely collaborate with partner teams when engineering & business dependencies exist

Successful candidates must have:
- Experience with relevant pre-silicon verification experience on IP/Subsystems/SoC architectures
- Proven experience of working through the full product cycle from initial design to final product
- Outstanding technical problem solving and debugging ability
- Excellent skills in a modern verification methodology such as UVM/OVM
- Strong understanding of SystemVerilog & Assertions
- Good development skills in C/C++ design and coding, with strong debugging skills
- Solid written and verbal communications skills
- Excellent project management skills and ability to juggle multiple projects at once
- A BS/MS in Electrical Engineering, Computer Engineering or equivalent
- Desire to be creative; Try. Fail. Learn. Try again.

Additional experience desired:
- Low power verification preferred
- Experience with Formal or property checking preferred
- High tolerance for ambiguity working through incubation to actual product definition to execution
- Experience with working in a team environment